





Features:

- Compliance Halogens Free
- (Br < 900 ppm, Cl < 900 ppm, Br+Cl < 1500 ppm) • Current transfer ratio
- (CTR: 50~600% at IF = 5mA, VCE = 5V)
- High isolation voltage between input and output (Viso = 5000Vrms)
- Creepage distance > 7.62mm
- Operating temperature up to +110°C
- Compact small outline package
- Compliance with EU REACH.
- •The product itself will remain within RoHS compliant version
- UL and cUL approved(No.E214129)
- VDE approved (No.132249)
- SEMKO approved
- NEMKO approved
- DEMKO approved
- FIMKO approved
- CQC approved

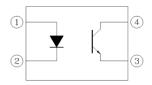
Description

The EL817-G series of devices each consist of an infrared emitting diodes, optically coupled to a phototransistor detector. They are packaged in a 4-pin DIP package and available in wide-lead spacing and SMD option.

Applications

- Programmable controllers
- System appliances, measuring instruments
- Telecommunication equipments
- Home appliances, such as fan heaters, etc.
- Signal transmission between circuits of different potentials and impedances

Schematic



Pin Configuration

- 1. Anode
- 2. Cathode
- 3. Emitter
- 4. Collector

Absolute Maximum Ratings (Ta=25°C)

	Parameter	Symbol	Rating	Unit
	Forward current	١ _F	60	mA
Input	Peak forward current (1us, pulse)	I _{FP}	1	А
	Reverse voltage	V _R	6	V
	Power dissipation	D	100	mW
	Derating factor (above $T_a = 100^{\circ}C$)	P _D	2.9	mW/°C
	Power dissipation	_	150	mW
Output	Derating factor (above $T_a = 100^{\circ}C$)	P _C	5.8	mW/°C
	Collector current	I _C	50	mA
	Collector-Emitter voltage	V _{CEO}	80	V
	Emitter-Collector voltage	V _{ECO}	7	V
Total Powe	Total Power Dissipation		200	mW
Isolation V	lation Voltage ^{*1} V _{ISO} 5000		5000	V rms
Operating	Operating Temperature		-55 to 110	°C
Storage Te	emperature	T _{STG}	T _{STG} -55 to 125 °	
Soldering	Temperature* ²	T _{SOL}	260	°C

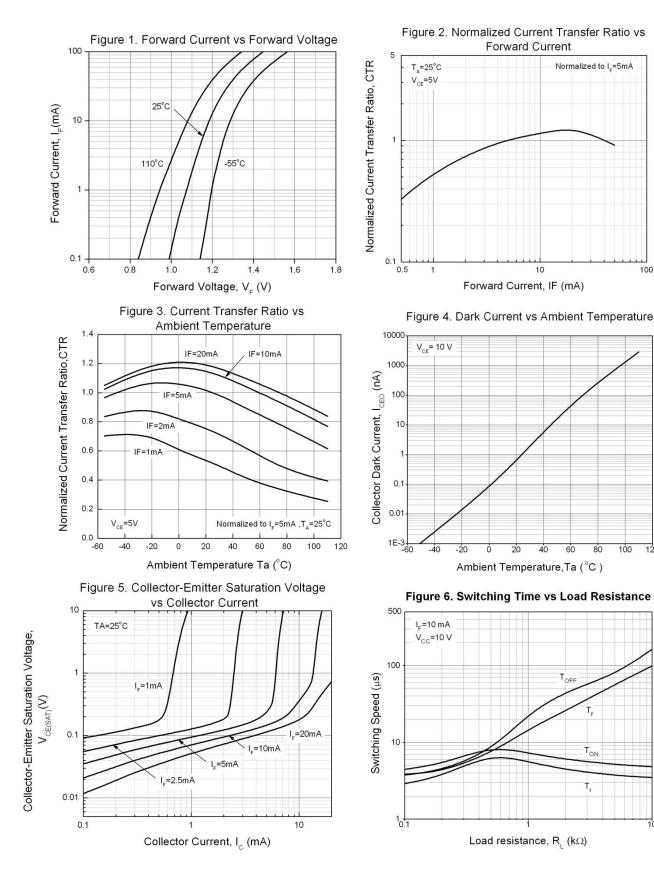
Notes:

*1 AC for 1 minute, R.H.= 40 ~ 60% R.H. In this test, pins 1, 2 are shorted together, and pins 3, 4 are shorted together. *2 For 10 seconds

Electro-Optical Characteristics (Ta=25°C unless specified otherwise)

Input							
Parameter		Symbol	Min.	Тур.	Max.	Unit	Condition
Forward Voltage		V _F	-	1.2	1.4	V	I _F = 20mA
Reverse Cu	irrent	I _R	-	-	10	μA	$V_R = 4V$
Input capac	itance	Cin	-	30	250	pF	V = 0, f = 1kHz
Dutput							
Parar	neter	Symbol	Min	Тур.	Max.	Unit	Condition
Collector-Er current	nitter dark	I _{CEO}	-	-	100	nA	V _{CE} = 20V, I _F = 0mA
Collector-Ei breakdown		BV _{CEO}	80	-	-	V	I _C = 0.1mA
Emitter-Collector breakdown voltage		BV_{ECO}	7	-	-	V	$I_E = 0.1 \text{mA}$
ransfer Ch	aracteristics						
Parar	neter	Symbol	Min	Тур.	Max.	Unit	Condition
	EL817	CTR	50	-	600	%	I _F = 5mA ,V _{CE} = 5V
	EL817A		80	-	160		
Current	EL817B		130	-	260		
Transfer	EL817C		200	-	400		
ratio	EL817D		300	-	600		
	EL817X		100	-	200		
	EL817Y		150	-	300		
Collector-Er saturation v		V _{CE(sat)}	-	0.1	0.2	V	$I_{\rm F} = 20 {\rm mA}$, $I_{\rm C} = 1 {\rm mA}$
Isolation resistance		R _{IO}	5×10 ¹⁰	-	-	Ω	V _{IO} = 500Vdc, 40~60% R.H.
Floating capacitance		C _{IO}	-	0.6	1.0	pF	$V_{IO} = 0$, f = 1MHz
Cut-off frequency		fc	-	80	-	kHz	$V_{CE} = 5V, I_C = 2mA$ $R_L = 100\Omega, -3dB$
Rise time		tr	-	6	18	μs	$V_{CE} = 2V, I_C = 2mA,$
Fall time		t _f	-	8	18	μs	R _L = 100Ω

* Typical values at $T_a = 25^{\circ}C$



Normalized to I_F=5mA

10

40

60 80

TOFF

T,

TON

T,

100 120

100

Typical Electro-Optical Characteristics Curves

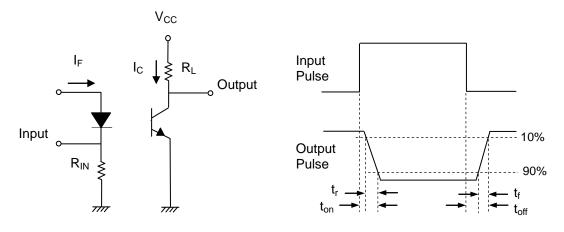


Figure 7. Switching Time Test Circuit & Waveforms

Order Information

Part Number

EL817X(Y)(Z)-FVG

Note

- X = Lead form option (S1, S2, M or none)
- Y = CTR Rank (A, B, C, D, X, Y or none)
- Z = Tape and reel option (TU, TD or none)
- F = Lead frame option (F: Iron, None: copper)
- V = VDE safety (optional)
- G = Halogens free

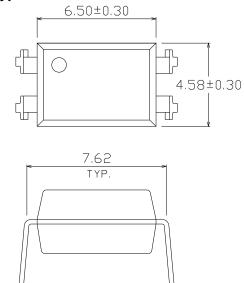
Option	Description	Packing quantity
None	Standard DIP-4	100 units per tube
М	Wide lead bend (0.4 inch spacing)	100 units per tube
S1 (TU)	Surface mount lead form (low profile) + TU tape & reel option	1500 units per reel
S1 (TD)	Surface mount lead form (low profile) + TD tape & reel option	1500 units per reel
S2 (TU)	Surface mount lead form (low profile) + TU tape & reel option	2000 units per reel
S2 (TD)	Surface mount lead form (low profile) + TD tape & reel option	2000 units per reel

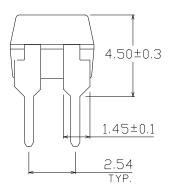
0.25

7.62~9.5

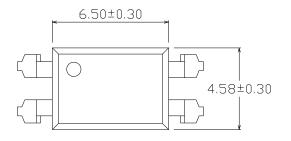
Package Dimension (Dimensions in mm)

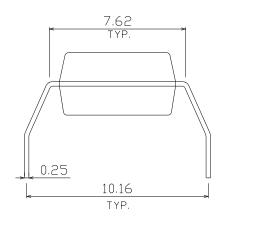
Standard DIP Type

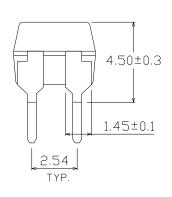




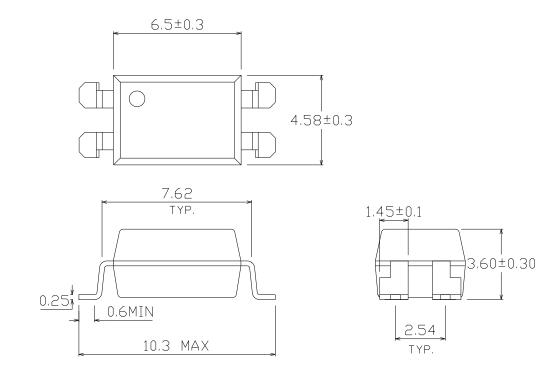
Option M Type



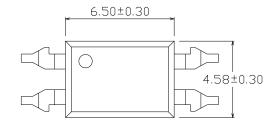


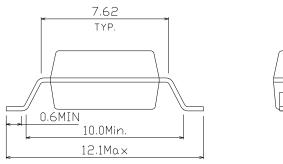


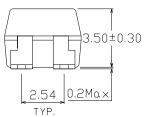
Option S1 Type



Option S2 Type

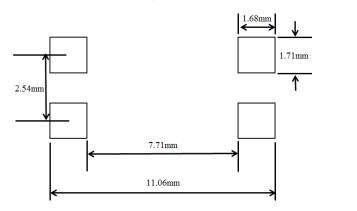


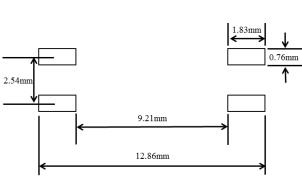




Recommended pad layout for surface mount leadform

For S1 option





For S2 option

Notes

Suggested pad dimension is just for reference only. Please modify the pad dimension based on individual need.

Device Marking

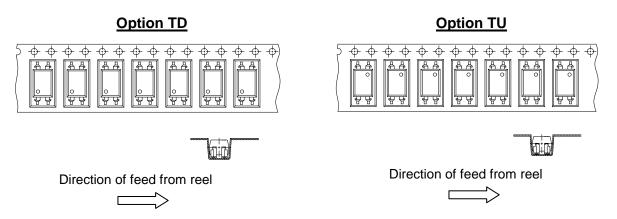




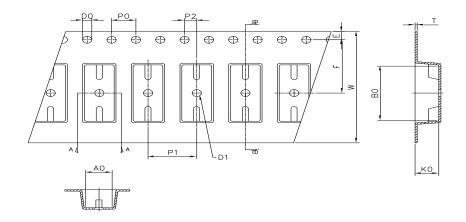
Notes

EL	denotes XI BNANG 817
denotes [Device Number
F	denotes Factory Code (G: China and Green part)
R	denotes CTR Rank (A, B, C, D, X, Y or none) Y
denotes 1	I digit Year code
WW	denotes 2 digit Week code
V	denotes VDE (optional)

Tape & Reel Packing Specifications



Tape dimensions

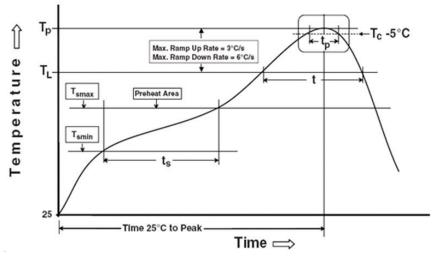


Dimension No.	Ao	Во	Do	D1	Е	F
Dimension (mm) S1	4.90±0.1	10.40±0.1	1.5±0.1	1.50±0.1	1.75±0.1	7.50±0.1
Dimension (mm) S2	4.88±0.1	12.55±0.1	1.5±0.1	1.50±0.1	1.75±0.1	11.5±0.1
Dimension No.	Ро	P1	P2	t	w	Ко
Dimension No. Dimension (mm) S1	Po 4.00±0.1	P1 8.00±0.1	P2 2.00±0.1	t 0.40±0.1	W 16.00±0.3	Ko 4.60±0.1

Precautions for Use

1. Soldering Condition

1.1 (A) Maximum Body Case Temperature Profile for evaluation of Reflow Profile



Note:

.

11

Preheat

Temperature min (T _{smin})	150 °C
Temperature max (T _{smax})	200°C
Time (T_{smin} to T_{smax}) (t_s) Average ramp-up rate (T_{smax} to T_p)	60-120 seconds 3 °C/second max
Other	
Liquidus Temperature (T_L)	217 °C
Time above Liquidus Temperature (t $_{L}$)	60-100 sec
Peak Temperature (T _P)	260°C
Time within 5 °C of Actual Peak Temperature: T_P - 5°C	30 s
Ramp- Down Rate from Peak Temperature	6°C /second max.
Time 25°C to peak temperature Reflow times	8 minutes max. 3 times

Reference: IPC/JEDEC J-STD-020D

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