

# 标准&定制开关连接器产品制造商

DONG GUAN XI BANG ELECTRONICS CO., LTD.



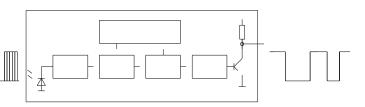


Fig.1 Block Diagram



Pin Configuration

- 1. Out
- 2. Gnd
- 3. Vcc



#### **Features**

- · Circular lens for improved reception characteristics
- Available for various carrier frequencies
- Low operating voltage and low power consumption
- · Optimized immunity against TFT backlight interferences
- · High immunity against ambient light
- Long reception range
- High sensitivity
- Pb free and RoHS compliant
- Compliance with EU REACH
- Compliance Halogen Free (Br < 900 ppm, Cl < 900 ppm, Br+Cl < 1500 ppm)</li>

### **Description**

The IRM-36xxJ15 devices are DIP type infrared receivers which have been developed and designed by using the latest IC technology, providing compatibility to most common IR protocols.

The PIN diode and preamplifier are assembled onto a lead frame and molded into a black epoxy package which operates as an IR filter. The demodulated output signal can directly be decoded by a microprocessor.

#### **Applications**

- AV equipment such as TV, VCR, DVD, CD, MD, etc.
- CATV set top boxes
- Multi-media Equipment
- · Other devices using IR remote control

#### Part number table

Model No.	Carrier Frequency fc	
IRM-3636J15	36 kHz	
IRM-3638J15	38 kHz	
IRM-3640J15	40 kHz	

## Absolute Maximum Ratings (T<sub>a</sub>=25°C) (note1)

Parameter	Symbol	Rating	Unit
Supply Voltage	Vcc	0 ~ 6	V
Output current	louт	0 ~ 2.5	mA
Operating Temperature	$T_{opr}$	-20 ~ +80	°C
Storage Temperature	$T_{stg}$	-40 ~ +85	°C
Soldering Temperature (note2)	T <sub>sol</sub>	260	°C

## Electro-Optical Characteristics (T<sub>a</sub>=25°C, V<sub>cc</sub>=5V)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition	
Current consumption	Icc	0.1	0.23	0.6	mA	No input signal	
Supply voltage	Vcc	2.7		5.5	V		
Peak wavelength	$\lambda_p$		940		nm		
High level output voltage	Vон	Vcc-0.4	Vcc		V	Output open	
Low level output voltage	Vol			0.5	V	I <sub>OUT</sub> < 2.5mA	
Internal pull up resistor	R <sub>PU</sub>		45		kΩ		
Max Reception range	L <sub>0max</sub>	14	20		_ m	Test signal	
	L <sub>45max</sub>	7	10			according to figure 2 Output pulse width: 400us <t<sub>L&lt;800us 400us<t<sub>H&lt;800us</t<sub></t<sub>	
Min reception distance	L <sub>0min</sub>			0.1			
Half angle(horizontal)	φh		±45		deg		
Half angle(vertical)	φν		±45		deg		
Output low pulse	TL	400		800	us	See chapter test method,	
Output high pulse	Тн	400		800	us		

Note1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur.

Note2: 4mm from mold body for less than 5 seconds

#### **Test method**

The specified electro-optical characteristics are valid under the following conditions.

- 1. Measurement environment must be a place without extreme reflections
- 2. Transmitter radiant intensity  $I_e = 80 \text{mW/sr}$
- 3. External lighting contains LED lighting with a color temperature of 6000K and illumination at the IR receiver is less than 100lux (Ev≤ 100Lux))
- 4. Test signal as shown below in figure 3

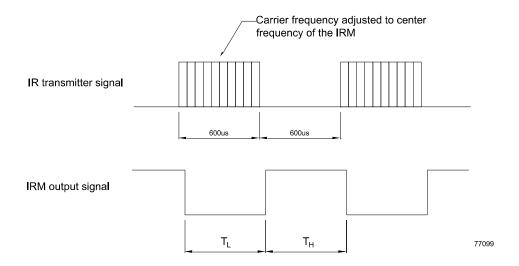


Fig.2 test signal and IRM output signal for reception distance and viewing angle test

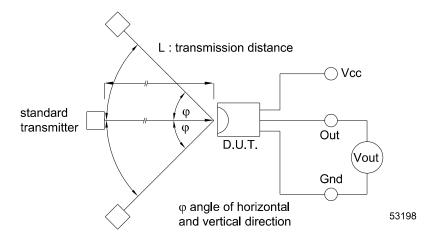


Fig.3 Measuring System

## **Typical Electro-Optical Characteristics Curves**

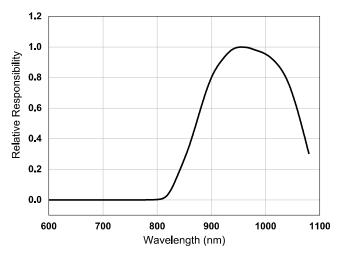


Fig. 4 Relative Responsibility vs. Wavelength

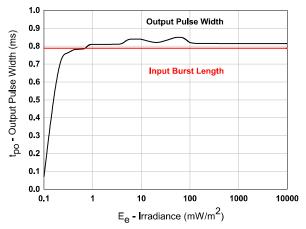


Fig. 6 Output Pulse Width and Sensitivity in Dark Ambient

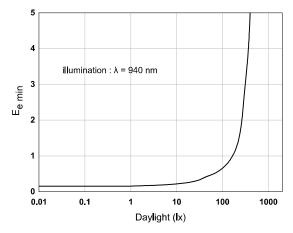


Fig. 8 Daylight vs.  $E_{e\,min}$ 

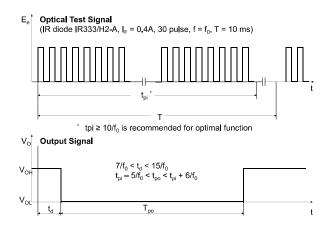


Fig. 5 IR Signal vs. Output Signal

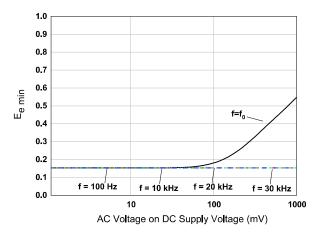


Fig. 7  $E_{e\,min.}$  vs. Supply Voltage Disturbances

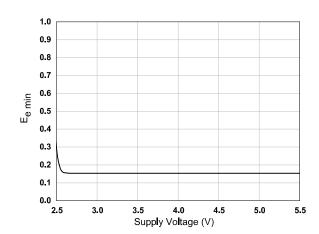


Fig. 9 Ee min. vs. Supply Voltage

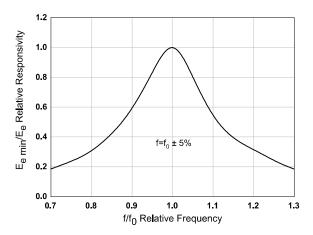


Fig. 10 Frequency Dependence of Responsivity

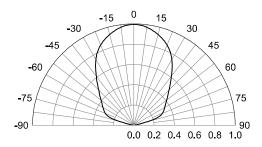


Fig. 12 Horizontal Directivity

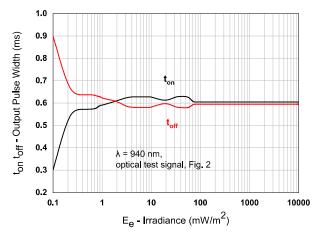


Fig. 11 Output Pulse Diagram

#### Application considerations

IRM IR receiver modules are high gain analog components to reach a long reception range. However, due to the high gain, they are also sensitive to noise from the power supply like  $V_{\rm cc}$  ripple. Noise on the power supply can reduce the reception range of the IRM or cause output glitches and corrupted data. To protect the IRM receiver from power supply noise, a RC filter must be connected as close as possible to the  $V_{\rm cc}$  and GND pins of the IRM. The circuit below in figure 9 shows the configuration of the RC filter and the required values. Ceramic or tantalum capacitor should be used, as standard electrolytic capacitors are only suitable for low frequencies and might not be able to filter noise in the frequency range of the IRM. The IRM receiver is most sensitive to noise which is at the carrier frequency or close to the carrier frequency. When using a switching mode power supply, the switching frequency must not be the same as the carrier frequency of the IRM. A gap of at least 20kHz between the switching frequency of the power supply and the IRM carrier frequency is recommended.

If the trace from the IRM output pin to the decoder IC on the PCB is long, the parasitic capacitance might be high causing slow rise times of the IRM output signal. In such case, an additional pull up resistor of 10kOhm or higher can be added at the IRM output to reduce the influence of parasitic trace capacitance.

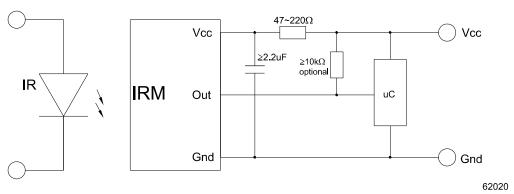


Fig.13: application circuit

#### Code compatibility

The IRM-36xxJ15 receiver modules are mainly designed for remote control applications which require very high noise immunity. Hence the IR code compatibility is matched for the most common IR protocols. To guarantee a proper data signal reception, a few points need to be taken into consideration.

The signal transmission must be carried out in data packages with limited length followed by a data pause time of a certain length. Continuous data transmission is not applicable as such kind of signal will be judged as noise and suppressed after a short time. Table1 below shows the compatibility to most commonly used IR protocols. If an IR protocol is not listed in this table, the compatibility needs to be checked according to the burst times, gap times, data package length and data pause time. The required limits for these items are shown in table 2 "acceptable IR signal timings".

Protocol	Suitable	Protocol	Suitable	Protocol	Suitable
NEC	Yes	Toshiba	Yes	Sony 12 Bit	Yes
RC5	Yes	RCMM	No	Sony 15 Bit	No
RC6	Yes	Continuous	No	Sony 20 Bit	No

Table 1: IR protocol compatibility

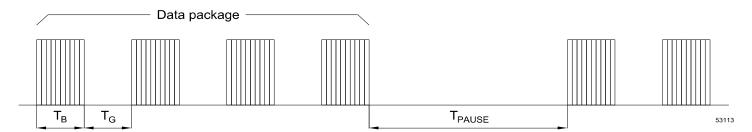


Fig.14: general IR data structure

	IRM-3636J15	
	IRM-3638J15	
	IRM-3640J15	
Min. burst length T <sub>B</sub>	300us	
Min. gap length T <sub>G</sub>	350us	
Min. data pause time TPause	Min. 25ms	
Required data pause time	>(Total word length / 3 )+18.5ms	

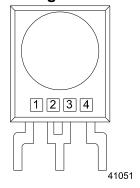
Table 2: acceptable IR timings

#### **Operation under noisy environment**

The IRM-36xxJ15 receiver modules are designed for high light noise immunity, especially for noise from fluorescent and energy saving lamps and noise from TFT TVs with CCFL backlight. The receiver is able to suppress most optical noise, but the presence of any kind of optical noise will cause shorter reception range because the AGC will reduce the gain to suppress the noise.

The presence of noise can also affect the output pulse jitter. In such case, the output pulse jitter shown in the electro-optical specification above, might not be valid anymore and bigger pulse jitter can occur. This behavior needs to be considered when tuning the timing limits of the decoder. It is recommended to use the output pulse variation shown in the electro-optical specifications above as a base to set the timing limits of the decoder. However, due to different protocols and environmental conditions, other timing limits might result in better performance and decoding security. This needs to be verified for the specific application by testing under different noise conditions.

#### **Device Marking**

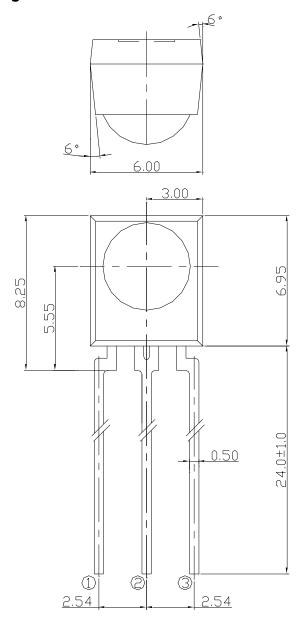


#### **Notes**

- 1 denotes Year code
- 2 denotes Month code
- 3 denotes Device number
- 4 denotes Carrier frequency

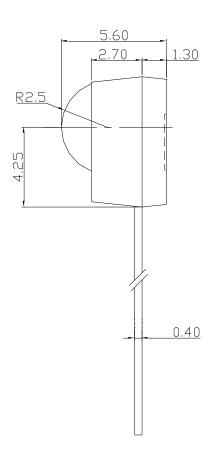


## **Package Dimenstions**



Pin Function

- ①: Vout
- ②: GND
- ③: Vcc



#### Notes:

- 1. All dimensions are in millimeters.
- 2. Tolerance unless otherwise mentioned is ±0.5mm

## **Packing Quantity**

250 pcs / Bag 6 Bags / Box 10 Boxes / Carton

#### **DISCLAIMER**

- 1. XI BNANG reserves the right(s) on the adjustment of product material mix for the specification.
- 2. The product meets XI BNANG published specification for a period of twelve (12) months from date of shipment.
- 3. The graphs shown in this datasheet are representing typical data only and do not show guaranteed values.
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