

A diagram of a 10-bit shift register. It consists of a horizontal row of 10 flip-flop symbols. The output of the 10th flip-flop is connected back to the input of the 1st flip-flop, forming a feedback loop. Below the flip-flops, the numbers 8 and 1 are shown, indicating the positions of the 8th and 1st bits respectively.

Technical drawing showing a hole in a part. The hole is dimensioned as $\varnothing 20.8 [0.819]$. The part has a width dimensioned as $19.40 [0.764]$. The text "钻孔尺寸" (Drilling Dimensions) is present.

MAX 2mm

								DSND		SCALE: N/A	MODEL TYPE: RJ JACK					
								DWN		VIEW: 	PART NO.: RJ45S-XKB08S					
								CHKD		UNIT: mm/in	DWG NO.: RJ45S-XKB08S					
								APPD		SIZE: A4						
REVISIONS						UNSPECIFIED TOLERANCES										
www.alpsr.com www.gdxjdz.com www.xbswitch.com								DONG GUAN XI BANG ELECTRONIC CO., LTD				WEIGHT	SHEET 1/1	REVISION A0		